

n bits of data by combining said successive groups of m bits of data, --;

line 26, delete "the temporarily stored" and insert -- said n bits of --.

Claim 2, line 20, after "multiplexing" insert -- n bits of --;

line 21, delete "serial" and insert -- successive groups of m bits of --;

line 22, delete "the serial" and insert -- said successive groups of m bits of --;

lines 22-23, delete "for writing thereof" and insert -- by performing plural write operations within a memory cycle --;

line 24, after "means" insert -- at a --.

Claim 3, line 25, after "transfer" insert -- m bits of --;

line 27, after "transfer of" insert -- successive groups of -- also delete "in parallel";

line 29, after "control means" insert -- by performing plural read/write operations within a memory cycle in said memory means --;

line 31, after "processor means" insert -- by combining said successive groups of m bits of data to form said n bits of data --.

Claim 4, line 12, delete "an" and insert -- and --;

line 13, delete "and" and insert -- in response to a request to retrieve pixel information from said data processing means at the specified addresses, --;

line 15, after "thereof" insert -- receiving processed pixel information from said data processing means, and storing processed pixel information in said memory means --.

Claim 8, line 21, after "storing" insert -- successive groups of m bits of --;

lines 21-22, delete "received on said memory bus";

line 22, before "from memory" insert -- read out from said memory means via said memory data bus by performing plural read operations within a memory cycle --;

line 23, delete "the memory location" and insert -- a -- also after "row" insert -- of said memory means --;

line 24, delete "with" and insert -- to -- also after "address" insert -- , forming n bits of data by combining said successive groups of said bits of data --;

line 25, delete "the temporarily stored" and insert -- said n bits of --;

line 28, after "multiplexing" insert -- n bits of --;

line 29, delete "serial" and insert -- successive groups of m bits of --;

line 30, delete "the serial" and insert --
said successive groups of m bits of -- also delete "serial";

line 31, delete "for writing thereof" and
insert -- by performing plural write operations within a
memory cycle--;

line 32, delete "memory location";

line 33, delete "with" and insert -- to --.

Claim 9, line 4, delete "the" and insert -- said --;
line 15, delete "graphic" and insert --
successive groups of m bits of also delete "sequentially"
also insert -- from said memory means -- after "read out";

line 16, delete "in a time shared fashion
from said memory means" and insert -- by performing plural
read operations within a memory cycle--;

line 17, after "control means" insert --
forms n bits of data by combining said successive groups of
m bits of data and -- also delete "the temporarily stored
graphic" and insert -- said n bits of --.

Claim 10, line 15, delete "graphic" and insert -- n
bits of --;

line 16, after "processing means" insert --
into successive groups of m bits of data";

line 17, delete "multiplexed graphic" and
insert -- successive groups of m bits of --;

lines 17-18, delete "sequentially in a time
shared fashion";

line 18, after "first bus" insert -- by
performing plural write operations within a memory cycle--.

Claim 11, line 3, delete "sequentially" and insert --
in successive groups of m bits of data--;

line 4, delete "in a time shared fashion";

line 6, delete "graphic" and insert -- n bits
of--;

line 7, delete "n bits of";

line 15, delete "sequential graphic data" and
insert -- said successive groups of m bits of data--;

line 17, after "means" insert -- by
performing plural read operations within a memory cycle--
also delete "parallel graphic data" and insert -- said n
bits of data-- also after "supplied" insert -- in parallel
--.

Claim 12, line 3, after "sequentially" insert -- as
successive groups of m bits of data--;

line 4, delete "in a time shared fashion"

line 7, delete "as" and insert -- in-- also
after "parallel insert -- as n bits of--;

line 14, after "means for" insert -- dividing
--;

line 15, delete "converting parallel graphic"
and insert -- said n bits of--;

line 16, delete "sequential graphic" and
insert -- successive groups of m bits of -- also delete
"supplied" and insert -- transferred -- also after "memory
means" insert -- by performing plural write operations
within a memory cycle --.

Claim 14, line 2, delete "segmented" and insert --
successive groups of --;

line 3, delete "sequentially";
lines 3-4, delete "in a time shared . . .
unit of time" and insert -- by performing plural read
operations within a memory cycle --.

Claim 16, line 2, after "wherein said" and insert --
successive groups of --.

Claim 18, line 2, delete "sequential" and insert --
successive groups of -- also after "converted" insert -- are
--;
lines 2-3, delete "is sequentially";
lines 3-4, delete "in a time shared . . .
unit of time" and insert -- by performing plural read
operations within a memory cycle --.

Claim 19, line 2, after "from said" insert --
successive groups of --;

line 3, delete "sequentially" also after
"memory means" insert -- by performing plural read operations within a memory cycle--;

line 5, delete "predetermined unit of time"
and insert -- memory cycle--.

Claim 20, line 2, after "wherein said" insert --
successive groups of--.

Claim 21, line 4, after "reading out" insert --
successive groups of-- also delete "sequentially";

line 5, delete "in a time shared fashion for"
and insert --by performing plural read operations within a memory cycle, -- also after "each" insert -- group of--
also after "m bits" insert -- of data being read out--;

line 8, delete "read out" and insert --
successive groups of--;

line 9, delete "parallel" also after "data"
insert -- by combining said successive groups of m bits of data--;

line 10, delete "converted" also after "bits of data" insert -- in parallel--.

Claim 22, line 6, after "data into" and insert --
successive groups of--;

line 7, after "of data" insert -- by dividing said n bits of data--;

line 8, after "converted" insert --
successive groups of -- also delete "sequentially";
line 9, delete "in a time shared fashion" and
insert by performing plural write operations within a
memory cycle--.

Claim 23, line 4, after "wherein" insert -- successive
groups of--;

line 5, delete "sequentially";
line 6, delete "in a time shared fashion" and
insert -- by performing plural read operations within a
memory cycle--;

line 13, delete "a plurality" and insert --
successive groups--;

line 14, after "thereto" insert -- by
combining said successive groups of m bits of data to form
said n bits of data and dividing said n bits of data to form
said successive groups of m bits of data--.

Claim 24, line 2, before "m bits of" insert --
successive groups of--;

lines 2-3, delete "is sequentially" and
insert -- are--;

lines 3-4, delete "in a time shared . . .
unit of time" and insert -- by performing plural read
operations within a memory cycle--.

Claim 25, line 4, delete "predetermined unit of time"
and insert -- memory cycle --.

Claim 26, line 2, before "m bits of data" insert --
successive groups of --.

Claim 28, line 4, after "reading out" insert --
successive groups of -- also delete "sequentially";
lines 4-5, delete "in a time . . . unit of
time" and insert -- by performing plural read operations
written on a memory cycle --;
line 7, after "read out" insert -- successive
groups of -- also after "parallel data" insert -- by
combining said successive groups of data --;
line 8, after "converted" insert -- parallel
--;
line 9, delete "predetermined unit of time"
and insert -- memory cycle --.

Claim 29, line 2, after "of said" insert -- successive
groups of -- also delete "sequentially" also after "said
memory" insert -- by performing plural read operations
within a memory cycle --.

Claim 30, line 1, after "28 wherein" insert -- each of
--;
line 2, after "data" insert -- successive
groups of -- also delete "sequentially" also after "said

memory" insert -- by performing plural read operations
within a memory cycle --.

Claim 32, line 6, delete "sequential" and insert --
successive groups of --;

line 8, after "said converted" insert --
successive groups of -- also delete "sequentially" also
after "said memory" insert -- by performing plural write
operations within a memory cycle --;

lines 9-10, delete "in a time . . . unit of
time" and insert -- wherein said memory cycle is --.

Claim 33, line 2, before "data received" insert --
parallel -- also delete "in parallel";

line 3, after "of data and" insert -- each of
-- also after "converted" insert -- successive groups of --
also delete "sequentially";

line 4, after "said memory" insert -- by
performing plural write operations within a memory cycle --.

Claim 44, line 4, after "graphic data" insert -- to be
stored in said memory --;

line 5, delete "the" also after "graphic
data" insert -- read out from --;

line 6, delete "stored in";

line 12, after "m bits" insert -- of --;

line 15, after "n bits" insert -- of --;

line 19, delete "a" and insert -- successive groups of m bits of data by performing plural read operations written in a memory cycle -- also delete "time shared fashion";

line 21, delete "supplying" and insert -- forming n bits of data using -- also delete "temporarily stored" and insert -- successive groups of m bits of data and supplying said n bits of data in parallel --;

line 22, delete "graphic data" also delete "as n bits parallel data" and insert -- through said second bus --;

line 24, delete "temporarily stored";

line 25, after "graphic data" insert -- temporarily stored in said memory --.

Claim 45, line 3, delete "the" and insert -- said -- also delete "graphic" and insert -- of --;

line 5, delete "a time shared fashion" and insert -- successive groups of m bits of data within a memory cycle --.

Claim 46, line 5, after "data processor" insert -- , to obtain successive groups of m bits of data --.

Claim 47, line 1, after "wherein" insert -- said successive groups of m bits of data --;

line 2, delete "graphic data";